



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

#6/a

Applicant(s): Ulrike Gruening et al.

Serial No.: 09/669,585

Group Art Unit: 2811

Filing Date: September 26, 2000

Examiner: C. Nguyen

10/23/02

Entitled: IMPROVED TRENCH CAPACITOR
MEMORY CELL

Smith

Docket No.: 00P7925US (IFX-060PUS)

Certificate of Mailing (37 C.F.R. 1.8(a))

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10-10-02

Date of Signature
and Mail Deposit

By: Tanya Blount
Tanya Blount

AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

In response to the Office Action date mailed May 17, 2002, please amend the above-identified application as follows:

In the Claims:

A clean copy of the amended claims is below, a comparison copy is attached:

Sub
a-1 B1

(Amended) A memory cell comprising:
a trench capacitor formed in a substrate;
a shallow transistor trench (STT) formed in the substrate;
a transistor comprising:
a first diffusion region, the first diffusion region couples the capacitor to
a gate of the transistor;

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